


INFORMATION DISCLOSURE
STATEMENT BY APPLICANT

 Form PTO-1449 (Modified)
 (Use several sheets if necessary)

Sheet

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of

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COMPLETE IF KNOWN	
Express Mail No.	EV335517629US
Application Number	10/663,897
Confirmation Number	4520
Filing Date	September 16, 2003
First Named Inventor	Gail A. Alverson
Group Art Unit	2157
Examiner Name	Unknown
Attorney Docket No.	324758001US3

U.S. PATENT DOCUMENTS					
Examiner Initials*	Cite No.	U.S. Patent or Application		Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		NUMBER	Kind Code (if known)		
KT		4,819,234		Huber	4/4/89
KT		4,872,167		Maezawa et al.	10/3/89
KT		5,168,554		Luke	12/1/92
KT		5,301,325		Benson	4/5/94
KT		5,333,280		Ishikawa et al.	7/26/94
KT		5,450,575		Sites	9/12/95
KT		5,504,932		Vassiliadis et al.	4/2/96
KT		5,533,192		Hawley et al.	7/2/96
KT		5,557,761		Chan	9/17/96
KT		5,564,051		Halliwell et al.	10/8/96
KT		5,581,764		Fitzgerald et al.	12/3/96
KT		5,594,864		Trauben	1/14/97
KT		5,598,560		Benson	1/28/97
KT		5,632,032		Ault et al.	5/20/97
KT		5,652,889		Sites	7/29/97
KT		5,712,996		Schepers	1/27/98
KT		5,754,855		Miller et al.	5/19/98
KT		5,768,591		Robinson	6/16/98
KT		5,768,592		Chang	6/16/98
KT		5,774,721		Robinson	6/30/98
KT		5,787,245		You et al.	7/28/98
KT		5,805,892		Nakajima	9/8/98
KT		5,812,811		Dubey et al.	9/22/98
KT		5,826,265		Van Huben et al.	10/20/98
KT		5,867,643		Sutton	2/2/99
KT		5,877,766		Bates et al.	3/2/99
KT		5,887,166		Mallick et al.	3/23/99
KT		5,901,315		Edwards et al.	5/4/99
KT		5,903,730		Asai et al.	5/11/99
KT		5,913,925		Kahle et al.	6/22/99
KT		5,953,530		Rishi et al.	9/14/99
KT		5,961,639		Mallick et al.	10/5/99
KT		5,966,539		Srivastava	10/12/99

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<i>Kenneth Jang</i>	<i>10/4/04</i>
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P A T E N T & T R A D E M A R K S
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KT		5,978,902		Mann	11/2/99	
KT		6,002,872		Alexander III et al.	12/14/99	
KT		6,002,879		Radigan et al.	12/14/99	
KT		6,009,269		Burrows et al.	12/28/99	
KT		6,029,005		Radigan	2/22/00	
KT		6,049,671		Slivka et al.	4/11/00	
KT		6,058,493		Talley	5/2/00	
KT		6,059,840		Click, Jr.	5/9/00	
KT		6,072,952		Janakiraman	6/6/00	
KT		6,094,716		Witt	7/25/00	
KT		6,101,524		Choi et al.	8/8/00	
KT		6,112,293		Witt	8/29/00	
KT		6,151,701		Humphreys et al.	11/21/00	
KT		6,151,704		Radigan	11/21/00	

FOREIGN PATENT DOCUMENTS								
Examiner Initials*	Cite No.	Foreign Patent or Application			Name of Patentee or Applicant of Cited Document	Date of Publication or Filing Date of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T
		Office	NUMBER	Kind Code (if known)				
KT		DE	DE19710252		Fujitsu Ltd.	2/26/98		
KT		DE	DE19710252		Fujitsu Ltd. (English Abstract)	2/26/98		
KT		EP	EP0422945		International Business Machines	4/17/91		
KT		EP	EP0455966		International Business Machines	11/13/91		
KT		EP	EP0537098		International Business Machines	4/14/93		
KT		EP	EP0855648		Texas Instruments, Inc.	7/29/98		
KT		EP	EP0864979		Digital Equipment Corporation	9/16/98		
KT		UK	GB2307760		AT&T Corp.	6/4/97		

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume/issue number(s), publisher, city and/or country where published.				T
KT		"Method of Tracing Events in Multi-Threaded OS/2 Applications," IBM Tech. Disclosure Bulletin, September 1993, pp. 19-22.				
KT		Adelberg, Brad et al., "The Strip Rule System for Efficiently Maintaining Derived Data," Sigmod Record, Association for Computing Machinery, New York, Vol. 26, No. 2, June 1, 1997.				
KT		Agrawal, Gagan et al., "Interprocedural Data Flow Based Optimizations for Compilation of Irregular Problems," Annual Workshop on Languages and Compilers for Parallel Computing, 1995.				
KT		Agrawal, Hiralal, "Dominators, Super Blocks and Program Coverage," 21st ACM SIGPLAN-SIGACT Symposium on Principles of Programming Languages, Portland, Oregon, January 17-21, 1994.				
KT		Alverson, Gail et al., "Processor Management in the Tera MTA System," 1995.				
KT		Alverson, Gail et al., "Scheduling on the Tera MTA," Job Scheduling Strategies for Parallel Processing, 1995.				
KT		Alverson, Gail et al., "Tera Hardware-Software Cooperation," Proceedings of Supercomputing 1997, San Jose, California, November 1997.				
KT		Alverson, Robert et al., "The Tera Computer System," Proceedings of 1990 ACM International Conference on Supercomputing, June 1990.				
KT		Anderson, Jennifer, et al., "Continuous Profiling: Where Have All The Cycles Gone?," Operating SystemsReview, ACM Headquarters, New York, Vol. 31, No. 5, December 1, 1997.				
KT		Bailey, D.H. et al., "The NAS Parallel Benchmarks – Summary and Preliminary Results," Numerical Aerodynamic Simulation (NAS) Systems Division, NASA Ames Research Center, California, 1991.				
KT		Briggs, Preston et al., "Coloring Heuristics for Register Allocation," Department of Computer Science, Rice University, Houston, Texas, June 1989.				
KT		Briggs, Preston et al., "Coloring Register Pairs," ACM Letters on Programming Languages and Systems, Vol. 1, No. 1, March 1992.				
KT		Briggs, Preston et al., "Effective Partial Redundancy Elimination," ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 29, No. 6, June 1, 1994.				
KT		Callahan, David et al., "A Future-Based Parallel Language for a General-Purpose Highly-Parallel Computer, Languages and Compilers for Parallel Computing, MIT Press, 1990.				

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS

Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume/issue number(s), publisher, city and/or country where published.		
Examiner Initials*	Cite No.	
KF		Callahan, David et al., "Improving Register Allocation for Subscripted Variables," Proceedings of the ACM SIGPLAN Conference on Programming Language Design and Implementation, White Plains, New York, June 20-22, 1990.
KF		Callahan, David et al., "Register Allocation via Hierarchical Graph Coloring," Proceedings of the ACM SIGPLAN '91 Conference on Programming Language Design and Implementation, Toronto, Canada, June 26-28, 1991.
KF		Callahan, David, "Recognizing and Parallelizing Bounded Recurrences," August 1991.
KF		Chow, Fred C. et al., "The Priority-Based Coloring Approach to Register Allocation," ACM Transactions on Programming Languages and Systems, Vol. 12, No. 4, October 1990, pp. 501-536.
KF		Click, Cliff, "Global Code Motion, Global Value Numbering," ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 30, No. 6, June 1, 1995.
KF		Cook, Jonathan et al., "Event Based Detection of Concurrency," SIGSOFT '98 ACM, 1998, pp. 34-45.
KF		Davidson, Jack W. et al., "Reducing the Cost of Branches by Using Registers," Proceedings of the 17th Annual Symposium on Computer Architecture, Seattle, Washington, May 28-31, 1990.
KF		Galarowicz, Jim et al., "Analyzing Message Passing Programs on the Cray T3E with PAT and VAMPIR," Research Report, "Online!", May 1998.
KF		Goldman, Kenneth, J., "Introduction to Data Structures," 1996, Retrieved from Internet http://www.cs.wustl.edu/~kjg/CS101_SP97/Notes?DataStructures/structures.html
KF		Hayashi, H. et al., "ALPHA: A High Performance Lisp Machine Equipped with a New Stack Structure and Garbage Collection System," 10 th Annual International Symposium on Computer Architecture, 1983.
KF		Kilte, Priyadarshan et al., "Load/Store Range Analysis for Global Register Allocation," ACM-SIGPLAN, June 1993.
KF		Knoop, Jens et al., "The Power of Assignment Motion," ACM SIGPLAN '95 Conference on Programming Language Design and Implementation, La Jolla, California, June 18-21, 1995.
KF		Korry, Richard et al., "Memory Management in the Tera MTA System," 1995.
KF		Lal, George et al., "Iterated Register Coalescing," ACM Transactions on Programming Languages and Systems, Vol. 18, No. 3, May 1996, pp. 300-324.

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KT		Lang, Tomas et al., "Reduced Register Saving/Restoring in Single-Window Register Files," Computer Architecture News, Vol. 14, No. 3, June 1986.	
KT		Linton, Mark A., "The Evolution of Dbx," USENIX Summer Conference, June 11-15, 1990.	
KT		Major System Characteristics of the TERA MTA, 1995.	
KT		Minwen, Ji et al., "Performance Measurements for Multithreaded Programs," SIGMETRICS '98, ACM, 1998, pp. 168-170.	
KT		Ram, A. et al., "Parallel Garbage Collection Without Synchronization Overhead," 12 th Annual Symposium on Computer Architecture, June 17, 1985.	
KT		Shim, SangMin et al., "Split-Path Enhanced Pipeline Scheduling for Loops with Control Flows, IEEE, December 2, 1998.	
KT		Smith, Burton, "Opportunities for Growth in High Performance Computing," November 1994.	
KT		Smith, Burton, "The End of Architecture," Keynote Address Presented at the 17 th Annual Symposium on Computer Architecture, Seattle, Washington, May 29, 1990.	
KT		Smith, Burton, "The Quest for General-Purpose Parallel Computing.	
KT		Sreedhar, Vugranam C. et al., "Incremental Computation of Dominator Trees," ACM SIGPLAN Notices, Association for Computing Machinery, New York, Vol. 30, No. 3, March 1, 1995.	
KT		Surajit, Chaudhuri et al., "An Overview of Data Warehousing and OLAP Technology," Sigmod Record, Association for Computing, New York, Vol. 26, No. 1, March 1997.	
KT		Tera MTA Principles of Operation, November 18, 1997.	
KT		Touzeau, Roy F., "A Fortran Compiler for the FPS-164 Scientific Computer," Proceedings of the ACM SIGPLAN '84 Symposium on Compiler Construction, SIGPLAN Notices 19(6):48-57, June 1984.	
KT		Tsai, Jenn-Yuan et al., "Performance Study of a Concurrent Multithreaded Processor," IEEE, 1998, pp. 24-35.	

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